Multiscale Characterization of Advanced Metallization for Microcrack Reduction

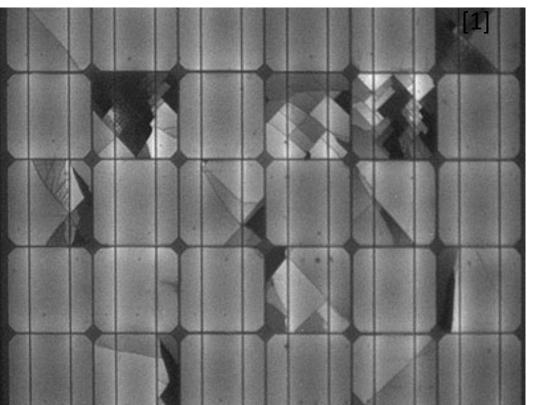
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Introduction

- >Cracking and micro defects are a significant cause of power losses and reduction in LCOE
- >Applied stresses from soldering and lamination process promote the formation of microcracks
- \rightarrow Operating conditions \rightarrow crack propagation >Most cracks are evident when cracks are of considerable size and/or affect performance
- > Need to correlate

Materials and processes \rightarrow Microcracks formation \rightarrow Performance through time



Tabbing and Encapsulation Testbed

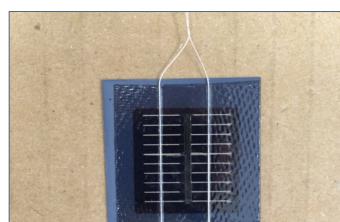
Dichroic Films and Encapsulants





Smart wire and soldering

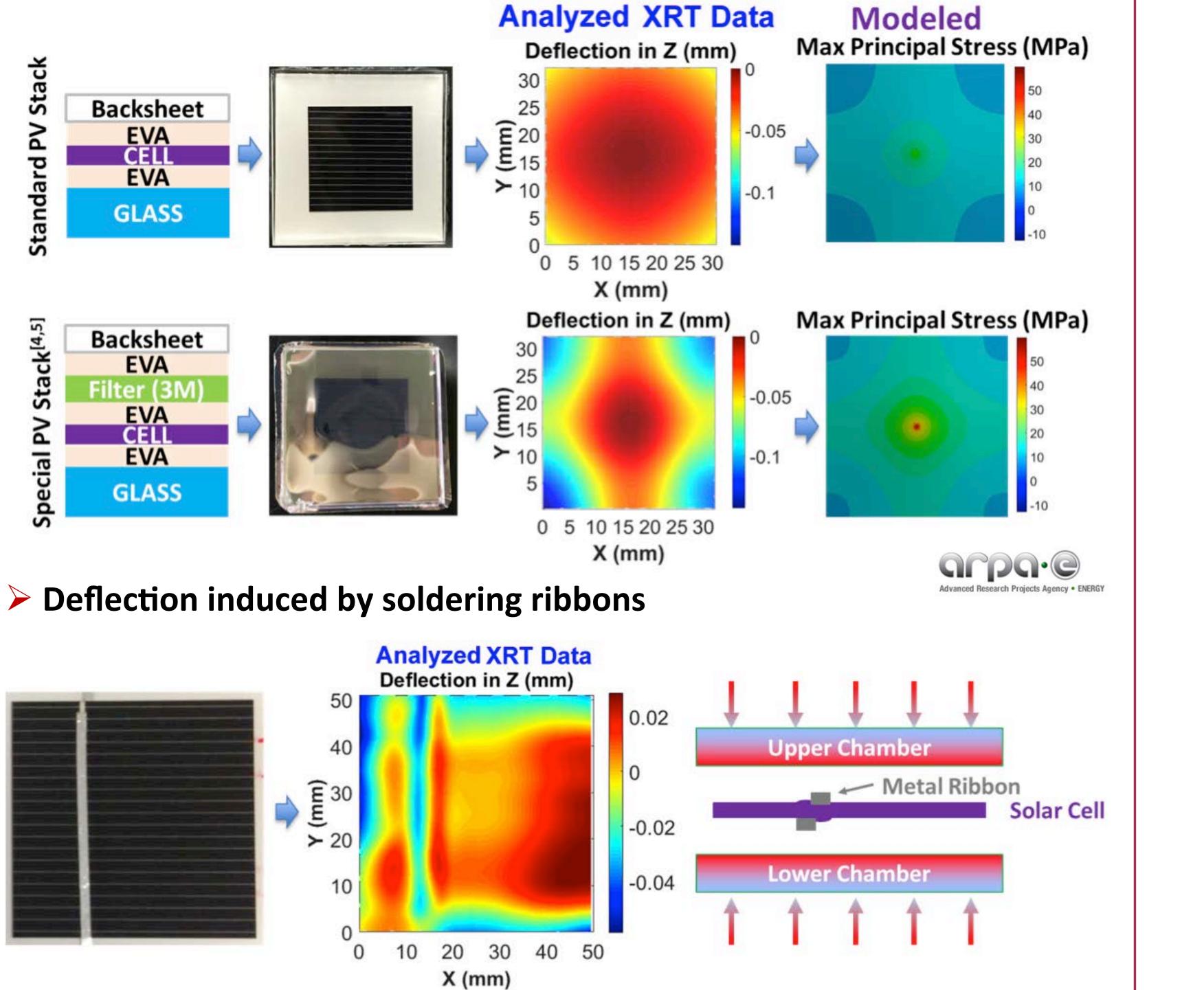








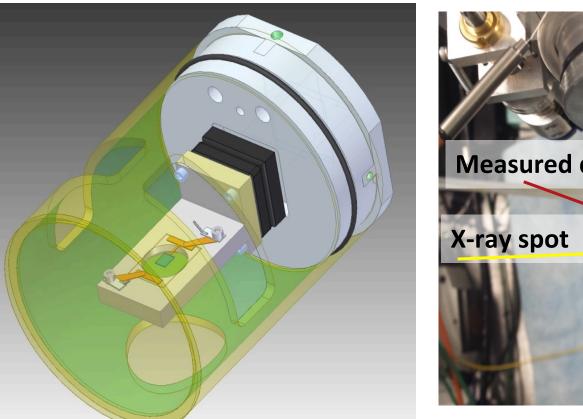
> Deflection comparison of different lamination stacks





Operando Studies

> Coupled structural and device performance studies in > Developments extended to degradation studies

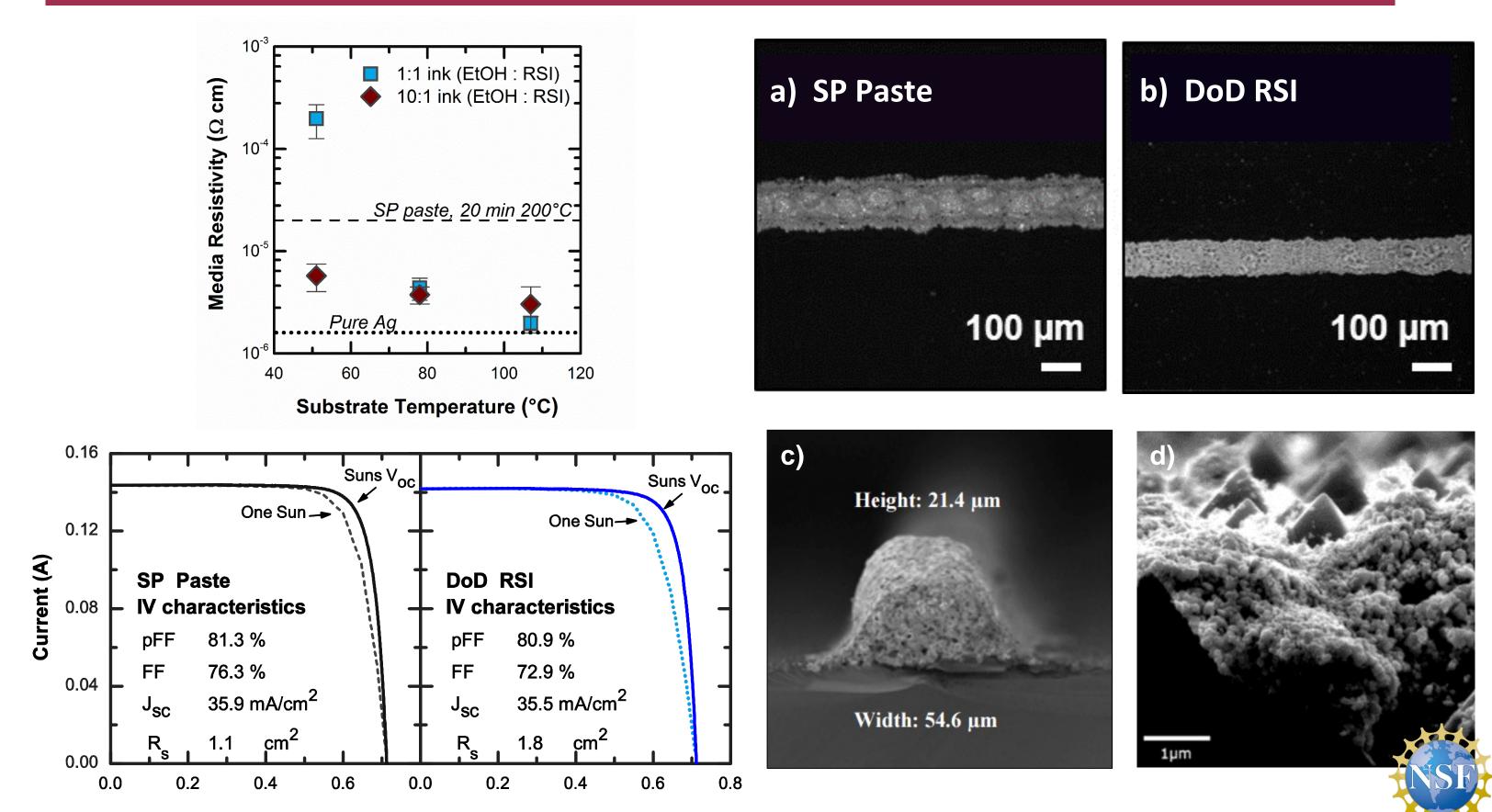


Heating Tape Thermocouple Measured device Light source (blocked

Operando chamber:

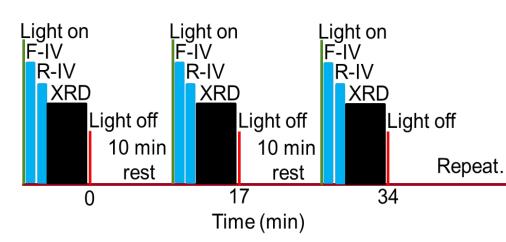
- White LED light (~ 1 Sun)
- Kapton heating tape
- He environment
- IV curves with light
- 1D and 2D XRD at temperature
- Thermocouple on top of device
- Understand the effects of aging & thermal cycling
- -New and existing module materials (e.g. encapsulants) > Operando Structural & Microstructural analysis

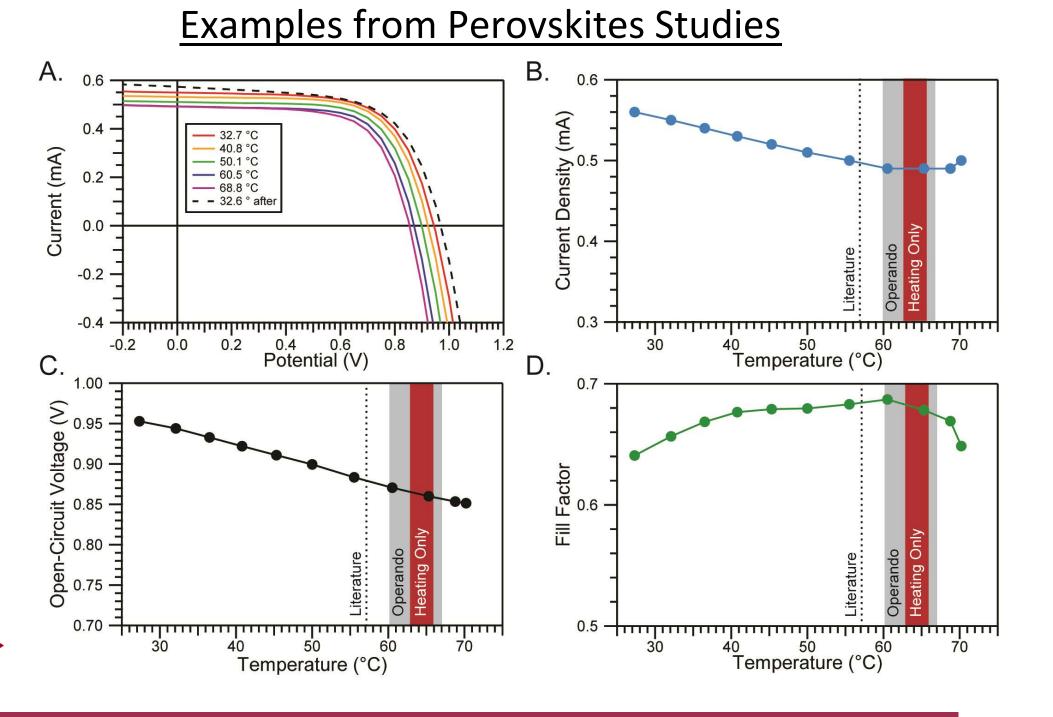
Novel Metallization



> Applied stressors

- -Atmosphere
- -Humidity
- -Temperature
- -Light
- -Electric field bias
- -Mechanical loading





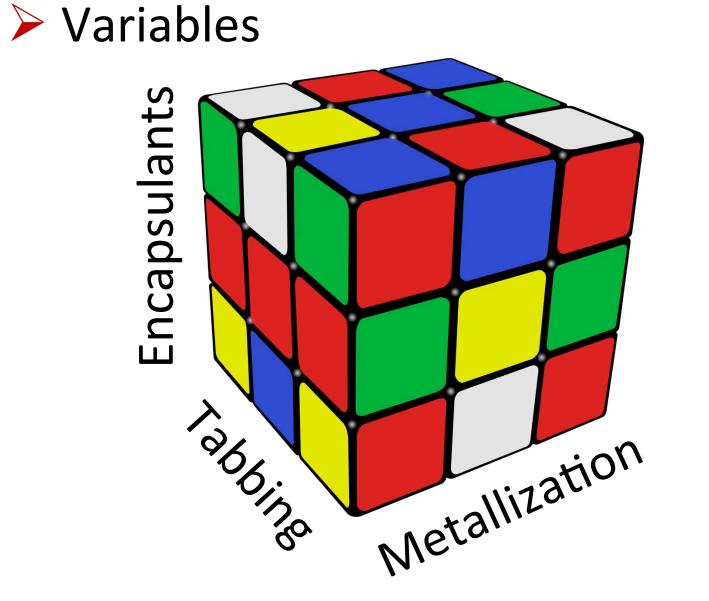
Proposed Work

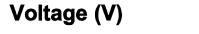
> Test

-Stress

Cracking

- > Applied Stressors
- Ambient
- Temperature -Microstructure
- Illumination -Performance
 - Mechanical loading









– Bias



